

Docket No.: 50090-240

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

: **RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE**

Kazuyuki NAKAGAWA, et al.

:

#11/B

Serial No.: 09/665,415

: Group Art Unit: 2811

1-7-03

Filed: September 20, 2000

: Examiner: Nitin Parekh

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For: SEMICONDUCTOR DEVICE HAVING AN IMPROVED MOUNTING STRUCTURE

AMENDMENT AND REQUEST FOR RECONSIDERATION

Box AF
Commissioner for Patents
Washington, DC 20231

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Sir:

The following Amendment and Remarks are submitted in response to the Office Action
dated July 24, 2002.

IN THE CLAIMS:

Please cancel claims 5 and 6, and amend claims 1 and 4 as follows:

- MISC B*
1. (Amended) A semiconductor device comprising:

a semiconductor element having a primary surface and a back surface, said semiconductor element having an element electrode on the primary surface; and
a circuit board having a primary surface and a back surface, said circuit board having a board electrode on at least the back surface, said circuit board having a predetermined opening hole formed therein;